

HARDWARE CHARACTERISTICS		UNIT	
Input voltage		VDC	24 ± 10%
Power input		W	36
Weight		g	975

CONTROL FEATURES			
Movement types			Synchronized and interpolated
Number of supported interpolation group			2
Max number of degrees of freedom (DOFs) per interpolation group			4 (X, Y, Z, Theta)
Embedded set point generator			G-code, PVT, PT
On board I/Os			8x RS-485 fast digital I/Os (software programmable, SPI ready)
TransnET real-time communication bus			1 Gbps
TransnET cycle time			100 µs
Number of supported axes on TransnET			Up to 63
Traces with pre-/post-trigger function			Yes
Master/Slave data exchange			Real-time at 10kHz (including slave to slave communication capability)
User programmability			Customer Software Module (CSM) running internally as a FreeRTOS application
Language			C program linked to embedded EDI libraries
Operating system			FreeRTOS
Customer Development environment			Xilinx SDK
Software Module (CSM) Number of dedicated cores			1 (ARM Cortex A53 at 1.3 GHz)
Ethernet dedicated port			1x TCP/IP (1 Gbps)
Dedicated SD card			1x (max. 32 GB)
Debug port			JTAG and TCP/IP
Communication interface	To PC		2x TCP/IP (up to 1 Gbps) and 1x cabled PCIe Gen1 (2.5 Gbps) using a x4 lane connector
	To AccurET controllers		TransnET
Software / programmability from host PC	ComET commissioning software		For commissioning / monitoring (for software compatibility, refer to the ComET manual)
	ETEL Device Interface (EDI)		DLL files for C / C++ / .NET (for software compatibility, refer to the EDI manual)
	Firmware update		Ethernet TCP/IP

